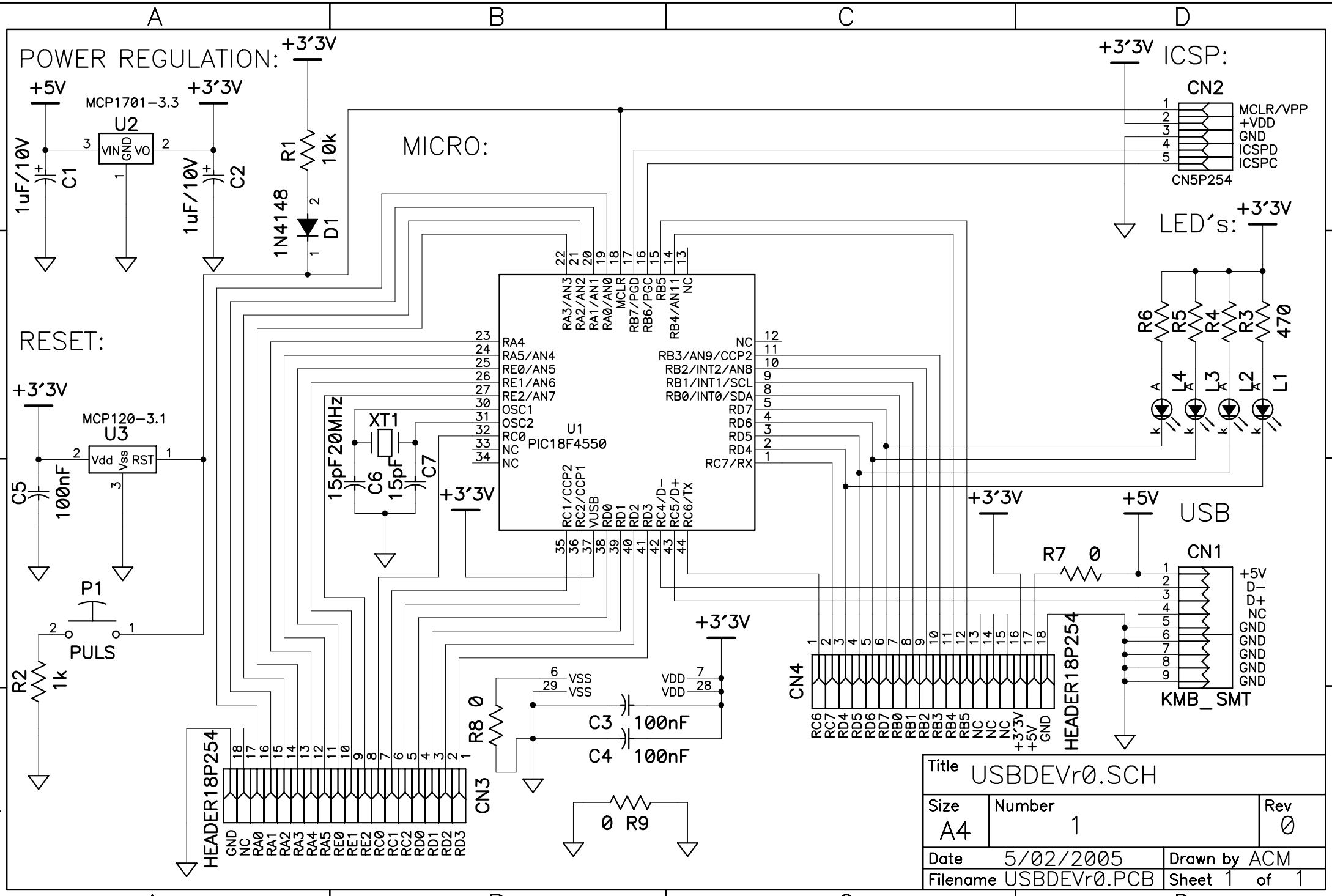
POWER REGULATION:
+5V
+3'3V
MCP1701-3.3
U2
VIN GND VO
1uF/10V
C1
C2
R1
10k
1N4148
D1
MICRO:
ICSP:
CN2
MCLR/VPP
+VDD
GND
ICSPD
ICSPC
CN5P254
LED's:
R6
R5
R4
R3
470
L4
L3
L2
L1
RESET:
MCP120-3.1
U3
Vdd Vss RST
C5
100nF
P1
PULS
R2
1k
XT1
20MHz
15pF
C6
C7
U1
PIC18F4550
RA3/AN3
RA2/AN2
RA1/AN1
RA0/AN0
MCLR
RB7/PGD
RB6/PGC
RB5
RB4/AN11
NC
RA4
RA5/AN4
RE0/AN5
RE1/AN6
RE2/AN7
OSC1
OSC2
RC0
NC
RB3/AN9/CCP2
RB2/INT2/AN8
RB1/INT1/SCL
RB0/INT0/SDA
RD7
RD6
RD5
RD4
RC7/RX
RC1/CCP2
RC2/CCP1
VUSB
RD0
RD1
RD2
RD3
RC4/D-
RC5/D+
RC6/TX
USB
R7 0
CN1
+5V
D-
D+
GND
KMB_SMT
CN4
HEADER18P254
RC6
RC7
RD4
RD5
RD6
RD7
RB0
RB1
RB2
RB3
RB4
RB5
+3'3V
+5V
VSS
VDD
R8 0
C3 100nF
C4 100nF
CN3
GND
RA0
RA1
RA2
RA3
RA4
RA5
RE0
RE1
RE2
RC0
RC1
RC2
RD0
RD1
RD2
RD3
0 R9
Title USBDEVr0.SCH
Size A4
Number 1
Rev 0
Date 5/02/2005
Drawn by ACM
Filename USBDEVr0.PCB
Sheet 1 of 1